RoHS

COMPLIANT

**GREEN** 

(5-2008)



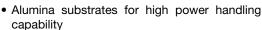
# SMD Wraparound Ultra Low Value Thin Film Resistors



With extremely low resistance and high power capabilities, these ultra low value resistors are available with solderable or weldable terminations.

### **FEATURES**

- NiCr + Ta<sub>2</sub>O<sub>5</sub> resistive layer
- · Pre-soldered or gold terminations
- No inductance for high frequency applications



• Resistance range: 0.1  $\Omega$  to 9.99  $\Omega$ 

• TCR down to 50 ppm/°C

Power rating: Up to 2 W at + 70 °C

• Withstand AEC-Q200 humidity test

 Material categorization: For definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

### Note

\* Lead (Pb)-containing terminations are not RoHS-compliant. Exemptions may apply.

STANDARD ELECTRICAL SPECIFICATIONS						
MODEL	SIZE	RESISTANCE RANGE $\Omega$	RATED POWER  P <sub>70 °C</sub> W	LIMITING ELEMENT VOLTAGE V	TOLERANCE ± %	TEMPERATURE COEFFICIENT ± ppm/°C
L0603	0603	0.1 to 9.99	0.125	50	1, 3, 5	50, 100, 200, 300
L0805	0805	0.1 to 9.99	0.2	50	1, 3, 5	50, 100, 200, 300
L1206	1206	0.1 to 9.99	0.33	50	1, 3, 5	50, 100, 200, 300
L1505	1505	0.1 to 9.99	0.5	50	1, 3, 5	50, 100, 200, 300
L2010	2010	0.1 to 9.99	1.0	50	1, 3, 5	50, 100, 200, 300
L2512	2512	0.1 to 9.99	2.0 (1)	50	1, 3, 5	50, 100, 200, 300

### Note

(1) With special assembly care

CLIMATIC SPECIFICATIONS				
Operating temperature range	- 55 °C; + 155 °C			

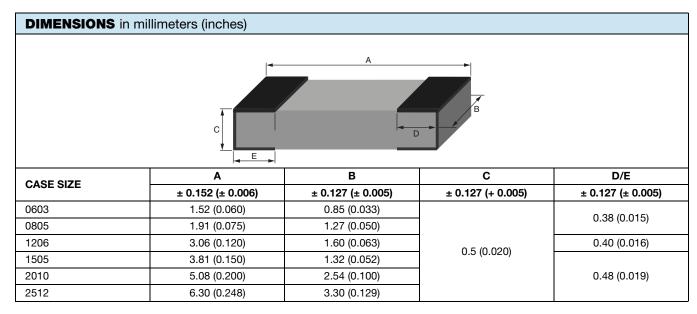
MECHANICAL SPECIFICATIONS				
Substrate	Alumina			
Technology	NiCr + Ta <sub>2</sub> O <sub>5</sub>			
Coating	Silicone			
Terminations	Solderable  B type: SnPb over nickel barrier  N type: SnAg over nickel barrier  G type: Gold over nickel barrier			

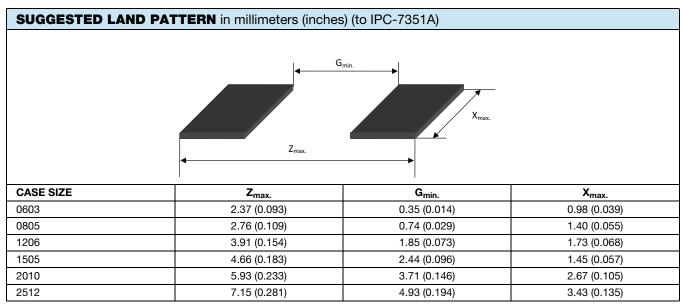
### Note

 Refer to Application Note "Guidelines for Vishay Sfernice Resistive and Inductive Components" (document number: 52029) for recommended reflow profile. Profile #3 applies.

TOLERANCE AND TCR VS. OHMIC VALUE					
OHMIC VALUE RANGE in $\Omega$	TIGHTEST TOLERANCE (%)	BEST TCR (ppm/°C)	TERMINATIONS		
0R1 < 0R25	1	300	N or B		
0R25 < 0R5	1	200	N or B		
0R5 < 2R5	1	100	N or B		
2R5 < 9R99	1	50	N or B		
0R1 < 0R25	5	300	G		
0R25 < 0R5	5	200	G		
0R < 1R	5	100	G		
1R < 2R5	3	100	G		
2R5 to 9R99	3	50	G		







### **Option: Enlarged Terminations: 0063**

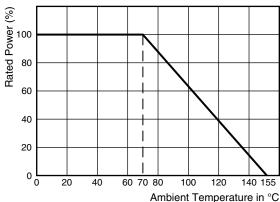
For stringent and special power dissipation requirements, the thermal resistance between the resistive layer and the solder joint can be reduced using enlarged terminations chip resistors which are soldered on large and thick copper pads acting as heat sinks (see application note: "Power Dissipation in High Precision Vishay Sfernice Chip Resistors and Arrays (P Thin Film, PRA Arrays, CHP Thick Film)": <a href="https://www.vishay.com/doc?53048">www.vishay.com/doc?53048</a>).

For enlarged terminations: Please consult Vishay Sfernice.

### Option: AEC-Q200 withstanding

Please order option 0058.

### **POWER DERATING CURVE**



**PACKAGING RULES** 

### Waffle Pack

Can be filled up to maximum quantity indicated in the table here above, taking into account the minimum order quantity. When quantity ordered exceeds maximum quantity of a single waffle pack, the waffle packs are stacked up on the top of each other and closed by one single cover.

To get "not stacked up" waffle pack in case of ordered quantity > maximum number of pieces per package: Please consult Vishay/Sfernice for specific ordering code.

### **PACKAGING**

Several types of packaging are proposed: waffle-pack and tape and reel

SIZE	MOQ	NUMBER PER PA	TAPE		
		WAFFLE PACK 2" × 2"	TAPE AND REEL		WIDTH
			MIN.	MAX.	
0603	100	100	100	5000	8 mm
0805				4000	
1206		140			
1505	100	60	100		
2010				2000	
2512		45			

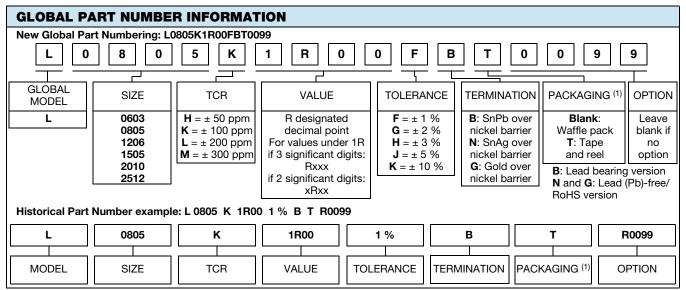
### **Tape and Reel**

Can be filled up to maximum quantity indicated in the table here above, taking into account the minimum order quantity. When quantity ordered is between the MOQ and the maximum reel capacity, only one reel is provided.

When several reels are needed for ordered quantity within MOQ and maximum reel capacity: Please consult Vishay Sfernice for specific ordering code.

PERFORMANCE					
		VALUES AND DRIFT			
TESTS	CONDITIONS	MIL-R-55342 REQUIREMENTS	TYPICAL PERFORMANCES		
Thermal shock	MIL-R-55342 C MIL-STD-702, method 107	± 0.25 %	± 0.02 %		
Short time overload	MIL-R-55342 C PARA 3.10.4.7.5	± 0.10 %	± 0.01 %		
Low temperature operation	MIL-R-55342 C PARA 3.9 and 4.7.4	± 0.25 %	± 0.01 %		
Resistance to solder heat	MIL-R-55342 C PARA 3.12, 4.7.7, 4.7.1.2	± 0.25 %	± 0.04 %		
Moisture resistance	MIL-R-55342 C PARA 3.13 and 4.7.8 MIL-STD-202, method 106	± 0.40 %	± 0.01 %		
MOISTURE resistance	AEC-Q200 85 °C/85 % RH/0.1 Pn 1000 h	-	Max. < 0.5 % + 0.05 Ω		
High temperature	MIL-R-55342 C PARA 3.11 and 4.7.6	± 0.20 %	± 0.075 %		
Load life	MIL-R-55342 C 2000 h Pn at 70 °C MIL-STD-202, method 108	± 0.50 %	± 0.15 %		

## Vishay Sfernice



Note

<sup>(1)</sup> For specific quantity of parts per packaging please consult Vishay Sfernice



## **Legal Disclaimer Notice**

Vishay

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## **Material Category Policy**

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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